

PTO/SB/08A (07-05)

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Substitute for form 1449/PTO Modified <div style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i></div>	<div style="text-align: right;">Complete if Known</div> Application Number: 10/055,241 Confirmation No.: 8631 Filing Date: October 29, 2001 First Named Inventor: Joseph E. Peck Art Unit: 2191 Examiner Name: Steelman, Mary J. Attorney Docket Number: 5150-63500
Sheet 1 of 1	

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number <small>Number-Kind Code (if known)</small>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-			
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document <small>Country Code-Number-Kind Code (if known)</small>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	Check if English Translation is attached
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, (page(s), volume-issue number(s), publisher, city and/or country where published.	Check if English Translation is attached
M	E1	X.-P. LING and H. AMANO; "WASMII: A Data Driven Computer on a Virtual Hardware," IEEE Workshop on FPGAs for Custom Computing Machines; 1993; April 1993; pps. 33 - 42; Napa, CA, U.S.A.	<input type="checkbox"/>
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Examiner Signature	Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application. ¹Applicant's unique citation designation number (optional).

Form PTO-1449 (modified)

List of Patents and Publications
For Applicant's Information
Disclosure Statement
(Use several sheets if necessary)

ATTY. DOCKET NO: 5150-63500

SERIAL NO: 10/055,241



APPLICANT: Peck et al.

FILING DATE: October 29, 2001

GROUP: 2191
2123**U.S. PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
MA	A1	4,849,880	7/18/89	Bhaskar et al.			
	A2	4,901,221	2/13/90	Kodosky et al.			
MA	A3	5,109,504	4/28/92	Littleton			
MA	A4	5,437,464	8/1/95	Terasima et al.			
MA	A5	5,497,498	3/5/96	Taylor			
MA	A6	5,535,342	7/9/96	Taylor			
MA	A7	5,555,201	09/10/1996	Dangelo et al.			
MA	A8	5,566,295	10/15/96	Cypher et al.			
MA	A9	5,583,749	12/10/96	Tredennick et al.			
MA	A10	5,603,043	02/11/1997	Taylor et al.			
	A11	5,603,043	2/11/97	Taylor et al.			
MA	A12	5,652,875	7/29/97	Taylor			
MA	A13	5,684,980	11/4/97	Casselman			
MA	A14	5,732,277	03/24/1998	Kodosky et al.			
MA	A15	5,784,275	7/21/98	Sojoodi et al.			
MA	A16	5,847,955	12/98	Mitchell et al.			
MA	A17	6,219,628	04/2001	Kodosky et al.			
MA	A18	6,226,776	05/01/2001	Panchul et al.			
MA	A19	6,230,307	05/08/2001	Davis et al.			
MA	A20	6,311,149	10/30/2001	Ryan et al.			

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FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO
MA	A21	WO 94 10627 A	5/11/94	PCT			
MA	A22	WO 94 15311 A	7/7/94	PCT			
MA	A23	WO 96 14618 A	5/17/96	PCT			

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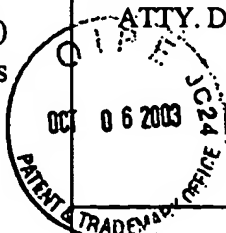
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List of Patents and Publications

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Disclosure Statement

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APPLICANT: Peck et al.

FILING DATE: October 29, 2001


GROUP: 2123

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>MX</i>	A24	Douglas, Bruce D., Ph.D., "Custom Embedded Communication Protocols, Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 637-657.
<i>MX</i>	A25	Labrosse, Jean, "Inside Real-Time Kernels," Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 205-214.
<i>MX</i>	A26	Labrosse, Jean, "Designing with Real-Time Kernels," Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 379-389.
<i>MX</i>	A27	Operating Systems, 2 nd Ed. By Williams Stallings, Prentice Hall, 1995, "Multiprocessor and Real-Time Scheduling", pp. 394-415.
<i>MX</i>	A28	International Search Report for PCT/US 98/10916 dated 9/17/98.
<i>MX</i>	A29	"LabVIEW USER MANUAL for Windows", National Instruments Co., pp. 4/1-21.
<i>MX</i>	A30	Beguelin et al., "Visualization and Debugging in a Heterogenous Environment," IEEE, pp. 88-95.
<i>MX</i>	A31	Jamal et al., "The Applicability of the Visual Programming Language LabView to Large Real-World Application," IEEE, pp. 99-106.
<i>MX</i>	A32	Per-Arne Wiberg, "Graphical Programming of Time-Deterministic Real-Time Systems," IEEE, pp. 173-180.
<i>MX</i>	A33	XP0002087124 Xiao-Yu, et al., "Software Environment for WASMII: A Data Driven Machine with a Virtual Harware," Field Programmable Logic Architectures, Synthesis and Applications, 4 th International Workshop on Field-Programmable Logic and Applications, FPL '94 Proceedings, Berlin Germany, 1994.
<i>MX</i>	A34	XP000554820 Edwards, et al., "Software acceleration using programmable hardware devices," IEEE Proceedings: Computers and Digital Techniques, Vol. 143, No. 1, January 1996, pp. 55-63.
<i>MX</i>	A35	XP000380758 Lesser, et al., "High Level Synthesis and Generating FPGAs with the BEDROC System," Journal of VLSI Signal Processing, Vol. 6, No. 2, August 1993, pp. 191-214.
<i>MX</i>	A36	International Search Report for PCT/US 98/13040 mailed 12/22/98.
<i>MX</i>	A37	Ade, M; Lauwereins, R; Peperstraete, J.A.; Hardware-Software Codesign with GRAPE, Proceedings of the Sixth IEEE International Workshop on Rapid System Prototyping, pp. 40-47, June 1995.
<i>MX</i>	A38	De Coster, GRAPE-II: An Introduction [online]. Automatic Control and Computer Architectures Department. Katholieke Universiteit Leuven, Belgium, February 22, 1996 [retrieved October 6, 1999] Retrieved from the Internet @ http://www.esat.kuleuven.ac.be/acca .

M. Shulman Considered 5-16-2006

Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)		ATTY. DOCKET NO: 5150-63500 OCT 06 2003 PATENT & TRADEMARK OFFICE		SERIAL NO: 10/055,241 RECEIVED OCT 09 2003 Technology Center 2100	
APPLICANT: Peck et al.				FILING DATE: October 29, 2001	
				GROUP: 2123	
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)					
ms	A39	Wenban et al., A Software Development System for FPGA-based Data Acquisition Systems, Proceedings of the IEEE Symposium on FPGAs for Custom Computing Machines, pp. 28-37, April 1996.			
ms	A40	Collamati et al. "Induction Machine Stator Fault On-line Diagnosis Based on LabVIEW Environment", Mediterranean Electrotechnical Conference, Vol. 1, pg. 495-498, May 1996.			
ms	A41	Spoelder et al., "Virtual Instrumentation: A Survey of Standards and Their Interrelation", Proc. IEEE Instr. And Measurement Tech. Conf., Vol. 1, pp. 676-681, May 1997.			
ms	A42	Srinivasan et al., "LabVIEW program Design for On-Line Data Acquisition and Predictive Maintenance", Proc. Of the 30th Southeastern Symp. On System Theory, pp. 520-524, March 1998.			
ms	A43	Wahidanabanu et al., "Virtual Instrumentation with Graphical Programming for Enhanced Detection and Monitoring of Partial Discharges", Proc. Electrical Insulation Conf. 1997, pp. 291-296, September 1997.			
ms	A44	Fountain: "Software advances in measurement and instrumentation"; IEE Coll. Software Instrumentation; pp. 4/1 to 4/45.			
XAMINER:		ms		DATE CONSIDERED: 5-16-2006	
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Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)	ATTY. DOCKET NO: 5150-63500	SERIAL NO: 10/055,241
		
	APPLICANT: Joseph E. Peck	
FILING DATE: October 29, 2001		GROUP: ²¹⁹¹ 2123

U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS


EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
MX	C1	DE 692 32 869 T2	Sept. 4, 2003	Germany			YES
MX	C2	DE 42 05 524 A1	Aug. 27, 1992	Germany			YES

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

MX	C3	Lauwereins, R; Engels, M; Ade, M; Peperstraette, J; Grape-II: A System-Level Prototyping Environment for DSP Applications, Computer, Vol. 28, Issue 2, pp. 35-43, 2/95.
MX	C4	Lysaght, P; Stockwood, J; A Simulation Tool for Dynamically Reconfigurable Field Programmable Gate Arrays, IEEE Transactions on Very Large Scale Integration Systems, Vol. 4, Issue 3, pp. 381-390, 9/96.
MX	C5	Petronino et al., An FPGA-based Data Acquisition System for a 95 GHz. W-band Radar, IEEE International Conference on Acoustics, Speech and Signal Processing, Vol. 5, pp. 4105-4108, April 1997.
MX	C6	Boulay et al., A High Throughput Controller for a 256-Channel Cardiac Potential Overlapping System, Canadian Conference on Electrical and Computer Engineering, Vol. 1, pp. 539-542, September 1995.

XAMINER: <i>M. Stetson</i>	DATE CONSIDERED: 6-22-05
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XAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)	ATTY. DOCKET NO: 5150-61502 	SERIAL NO: 10/058,150
	APPLICANT: Odom et al.	
	FILING DATE: October 29, 2001	GROUP: 2123

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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XAMINER:

M. Hutton

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